





**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Suresh Marisetty et al.

Title: SYSTEM ABSTRACTION LAYER, PROCESSOR ABSTRACTION LAYER, AND OPERATING SYSTEM ERROR HANDLING

|               |               |                   |                  |
|---------------|---------------|-------------------|------------------|
| Docket No.:   | 884.205US2    | Serial No.:       | 10/628,769       |
| Filed:        | July 28, 2003 | Due Date:         | January 21, 2009 |
| Examiner:     | Gabriel Chu   | Group Art Unit:   | 2114             |
| Customer No.: | 45457         | Confirmation No.: | 5779             |

Notice of Allowance Date: October 21, 2008

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